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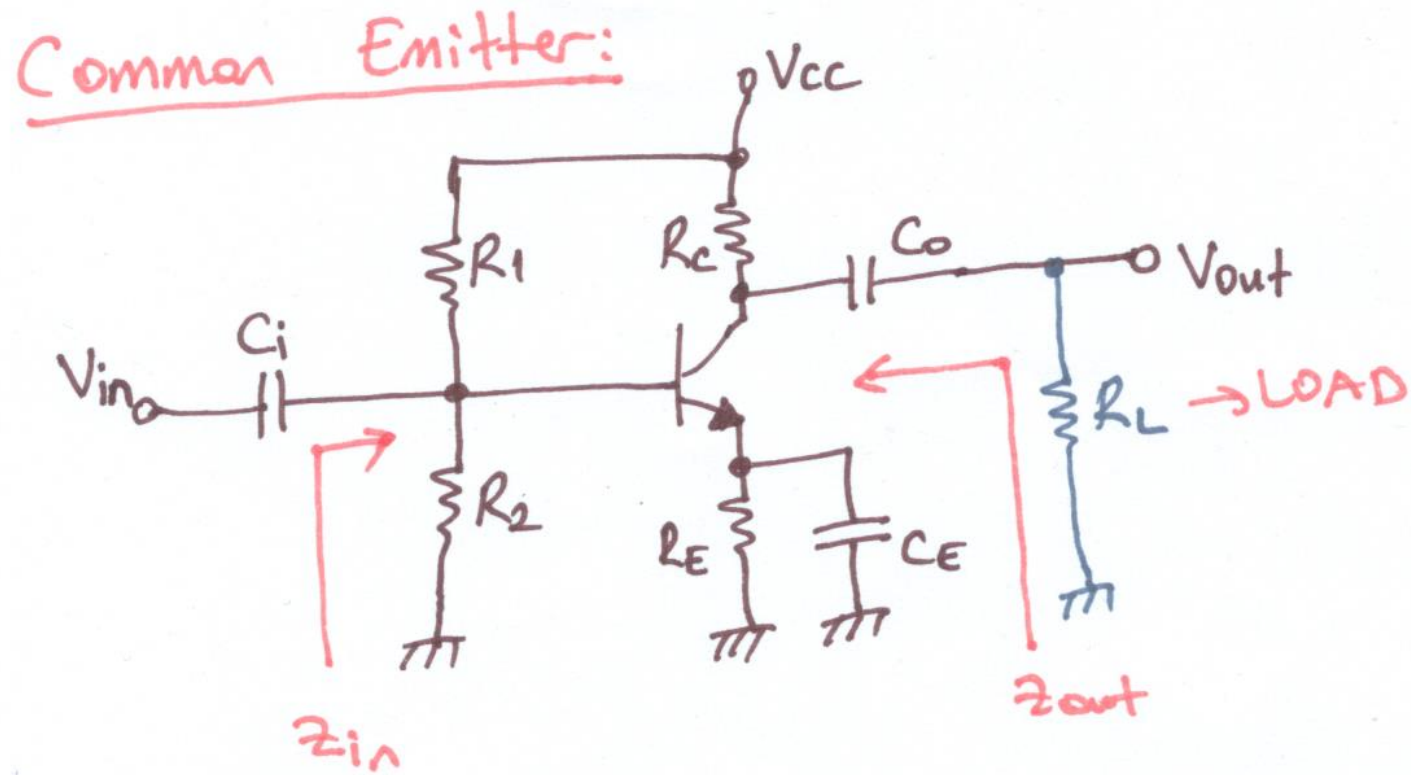
# Electronic Circuits

Lecture 4.2: Analysis of Common-Emitter Configuration BJT Amplifier

# Amplifier Analysis

- DC Analysis
  - $C \rightarrow$  open,  $L \rightarrow$  short, find  $r_e$  that is one of AC equivalent circuit parameters.
- AC Analysis (Mid-Frequency Analysis)
  - $C_s \rightarrow$  short,  $L_s \rightarrow$  open, DC sources eliminated, find  $A_v$ .
- AC Analysis (Low-Frequency Analysis)
  - DC sources eliminated,  $C_s$  and  $L_s$  are NOT eliminated, find  $f_c$  lower corner frequencies for HPF.
- AC Analysis (High-Frequency Analysis)
  - DC sources eliminated,  $C_s$  and  $L_s$  are eliminated, but new  $C_s$  are added, find  $f_c$  higher corner frequencies for LPF.
- Warning: This list possibly doesn't fit to Electronics-based department lectures.

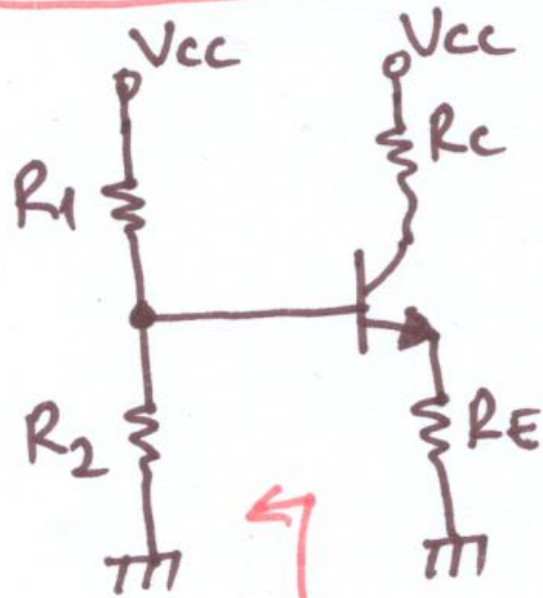
# Common-Emitter Amplifier: Circuit



$$A_V = \frac{V_{out}}{V_{in}} \quad (\text{Voltage gain})$$

# Common-Emitter Amplifier: DC Analysis (1)

## #1: DC Analysis:



Thevenin equivalent?

To determine DC operating point.

- All capacitors are open circuit.
- All inductors are short circuit.
- The main aim is to determine  $r_e$ !
- Other goals are  $I_{BQ}$ ,  $I_{CQ}$ ,  $I_{EQ}$ ,  $V_{CEQ}$ ,  $V_{CQ}$ .

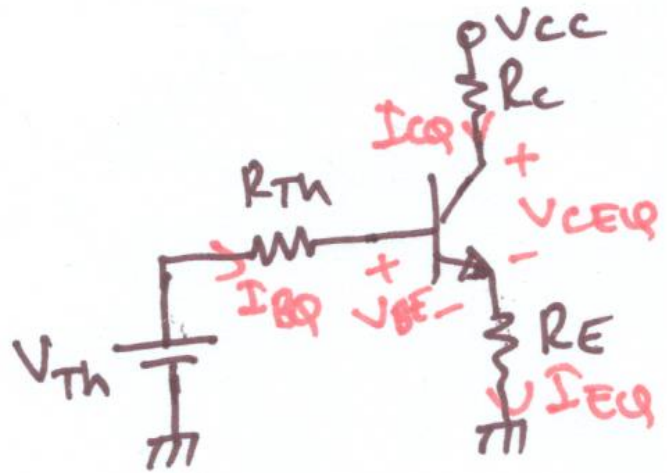
$$- I_E = I_B + I_C$$

$$- I_C = \beta \cdot I_B$$

$$- r_e = \frac{26 \text{ (mV)}}{I_{EQ} \text{ (mA)}}$$



## Common-Emitter Amplifier: DC Analysis (2)



$$V_{TH} = \frac{R_2}{R_1 + R_2} \cdot V_{CC}$$

$$R_{TH} = R_1 // R_2 = \frac{R_1 \cdot R_2}{R_1 + R_2}$$

$$-V_{TH} + R_{TH} \cdot I_{BQ} + V_{BE} + R_E \cdot I_{EQ} = 0$$

$$-R_E \cdot I_{EQ} - V_{CEQ} - R_C \cdot I_{CQ} + V_{CC} = 0$$

Find  $I_{BQ}$

Find  $V_{CEQ}$

Find  $I_{CQ}$

Find  $I_{EQ}$

Find  $r_e$

$$I_{CQ} = \beta \cdot I_{BQ}$$

$$I_{EQ} = I_{CQ} + I_{BQ} = (\beta + 1) \cdot I_{BQ}$$

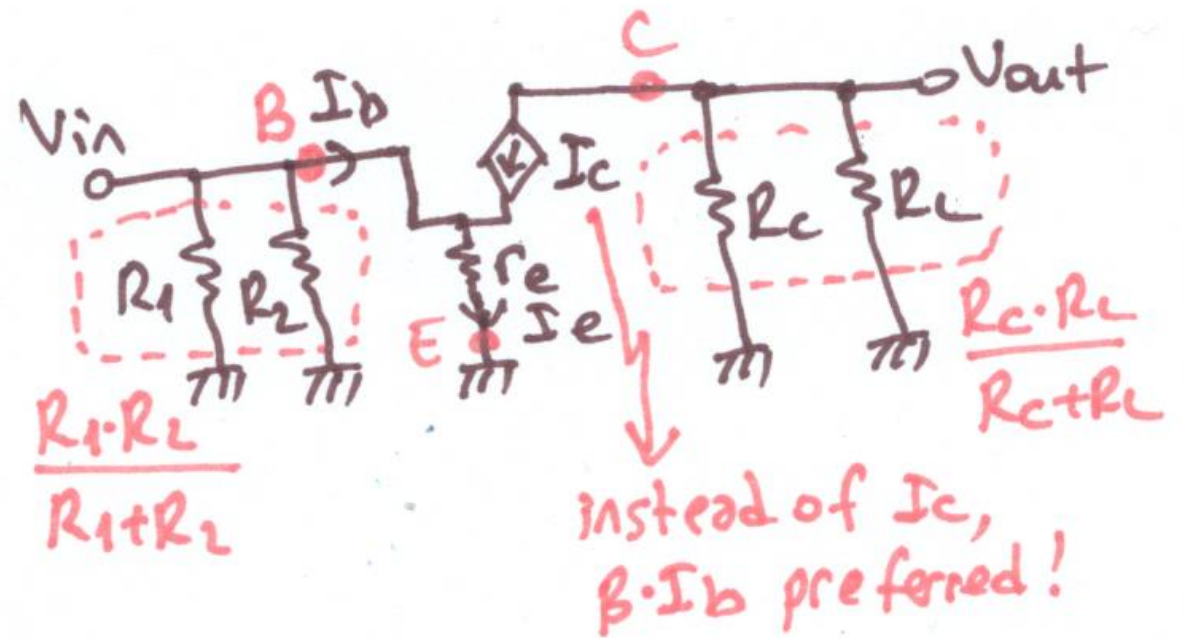
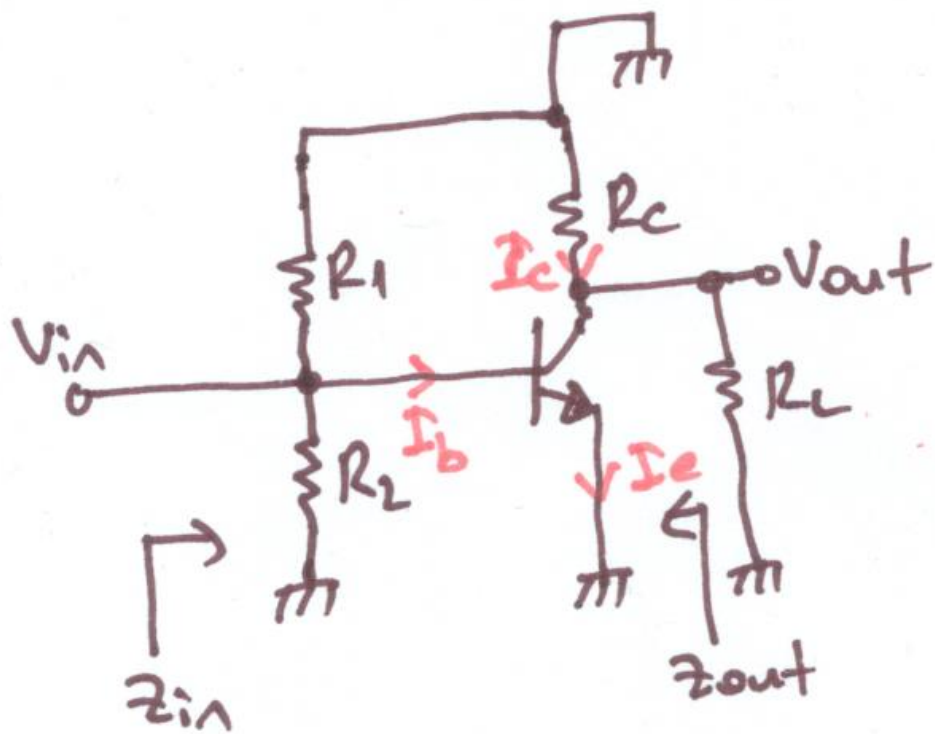
$$r_e = \frac{26 \text{ (mV)}}{I_{EQ} \text{ (mA)}} \approx$$

## Common-Emitter Amplifier: AC Analysis (1)

### #2: AC Analysis: (Mid-Frequency Analysis)

- To determine desired AC behavior.
- All capacitors are short circuit.
  - All inductors are open circuit.
  - The aim is to determine  $A_v$ ,  $Z_{in}$ ,  $Z_{out}$ !
  - DC sources are eliminated by voltages are short circuit and currents are open circuit.
  - We can use small-signal equivalent BJT model.
  - $r_e$  is from DC analysis!

## Common-Emitter Amplifier: AC Analysis (2)



## Common-Emitter Amplifier: AC Analysis (3)

$$V_{in} = r_e \cdot I_e$$
$$V_{out} = \frac{R_c \cdot R_L}{R_c + R_L} \cdot (-I_c)$$
$$A_v = \frac{V_{out}}{V_{in}} = - \frac{R_c \cdot R_L}{(R_c + R_L) \cdot r_e}$$

$$Z_{in} = R_1 \parallel R_2 \parallel ((\beta + 1) \cdot r_e)$$

The  $r_e$  is on the E side, so its effect on the B side is amplified by  $(\beta + 1)$ .

$$Z_{out} = R_c$$

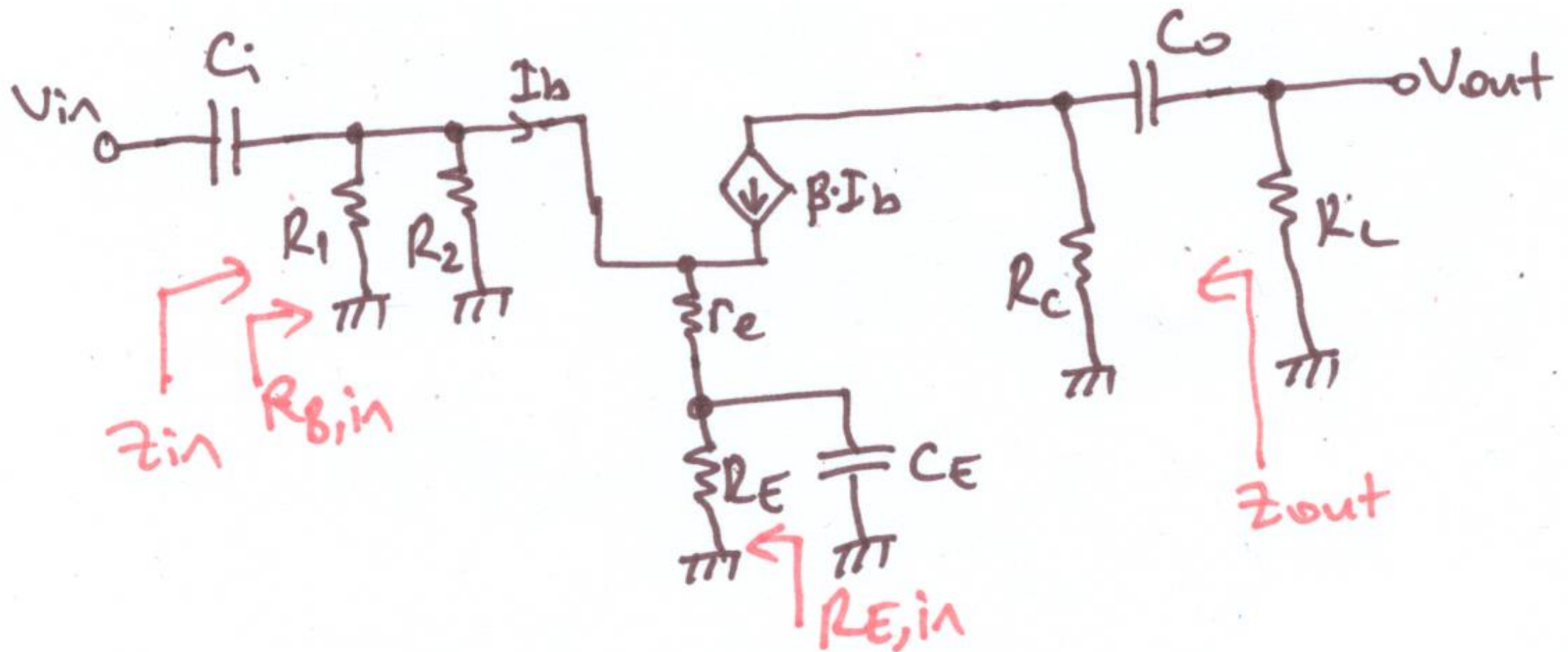
Since current source has resistance of  $\infty$  and  $R_L$  is the load not the part of the amplifier itself.



## Common-Emitter Amplifier: Low-Frequency Analysis (1)

- #3: LOW-FREQUENCY ANALYSIS:
- AC analysis, too.
  - But frequency is not enough high to make capacitors short- and inductors open-circuit.
  - Sources are eliminated, too.
  - We can use the model again.
  - We interested in local R-C sub-circuits!

## Common-Emitter Amplifier: Low-Frequency Analysis (2)

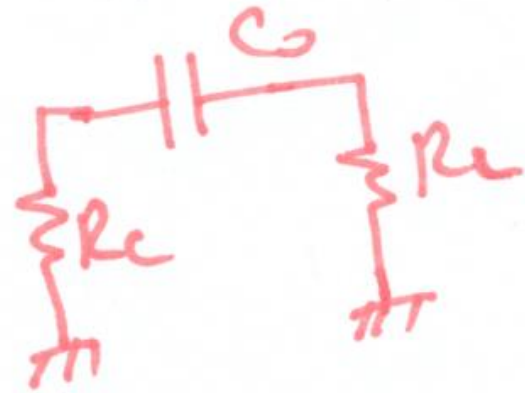


## Common-Emitter Amplifier: Low-Frequency Analysis (3)

$$f_{c1} = \frac{1}{2 \cdot \pi \cdot (R_c + R_L) \cdot C_o}$$

$$f_{c2} = \frac{1}{2 \cdot \pi \cdot R_{B,in} \cdot C_i}$$

$$R_{B,in} = R_{Th} // ((\beta + 1) \cdot r_e)$$



and

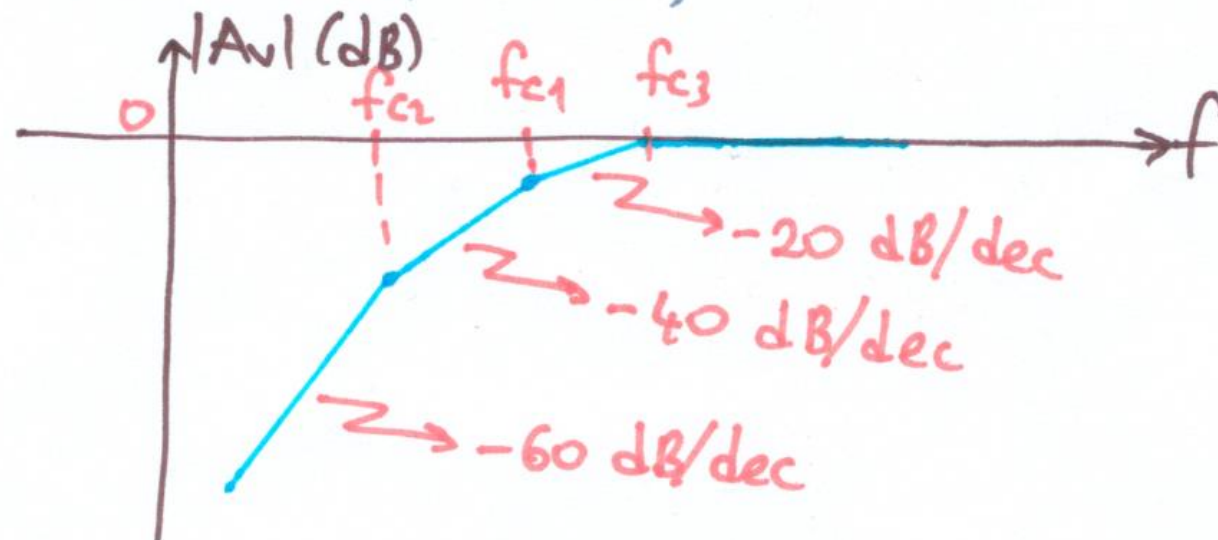
$$f_{c3} = \frac{1}{2 \cdot \pi \cdot R_{E,in} \cdot C_E}$$

$$R_{E,in} = R_E // \left( r_e + \frac{R_{Th}}{\beta + 1} \right)$$

## Common-Emitter Amplifier: Low-Frequency Analysis (4)

- There are some assumptions to simplify. But the more accurate result can be found via s-domain analysis 😊

If  $f_{c2} < f_{c1} < f_{c3}$ ,

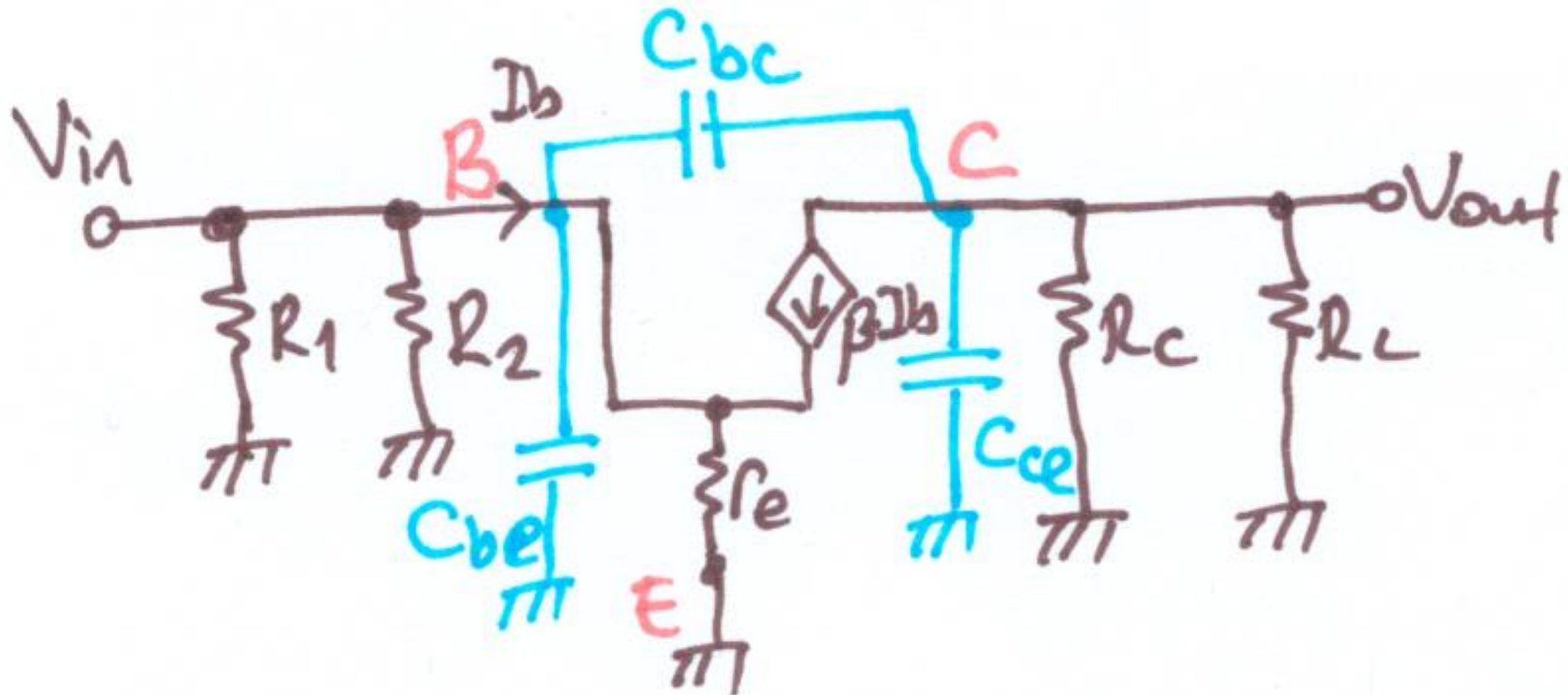




## Common-Emitter Amplifier: High-Frequency Analysis (1)

- #4: High-Frequency Analysis:
- AC analysis, too.
  - All capacitors are short-circuit.
  - All inductors are open-circuit.
  - All DC sources are eliminated.
  - But we have new capacitors between BJT leads:  $C_{be}, C_{bc}, C_{ce}$
  - We interested in local R-C sub-circuits!
- In fact  $\beta$  (or  $h_{fe}$ ) is the function of frequency!

## Common-Emitter Amplifier: High-Frequency Analysis (2)



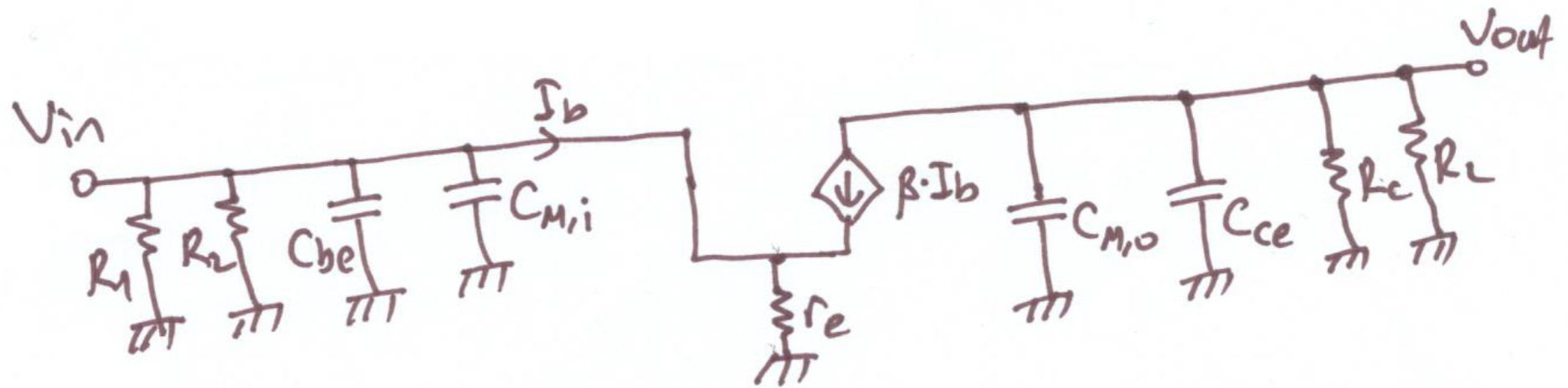
## Common-Emitter Amplifier: High-Frequency Analysis (3)

\* Miller Theorem: If there is a capacitor from input to output, we can add extra capacitor to the input and extra capacitor to the output by eliminating this capacitor.

$$C_{M,i} = (1 - A_v) \cdot C_{bc}$$

$$C_{M,o} = \left(1 - \frac{1}{A_v}\right) \cdot C_{bc}$$

## Common-Emitter Amplifier: High-Frequency Analysis (4)

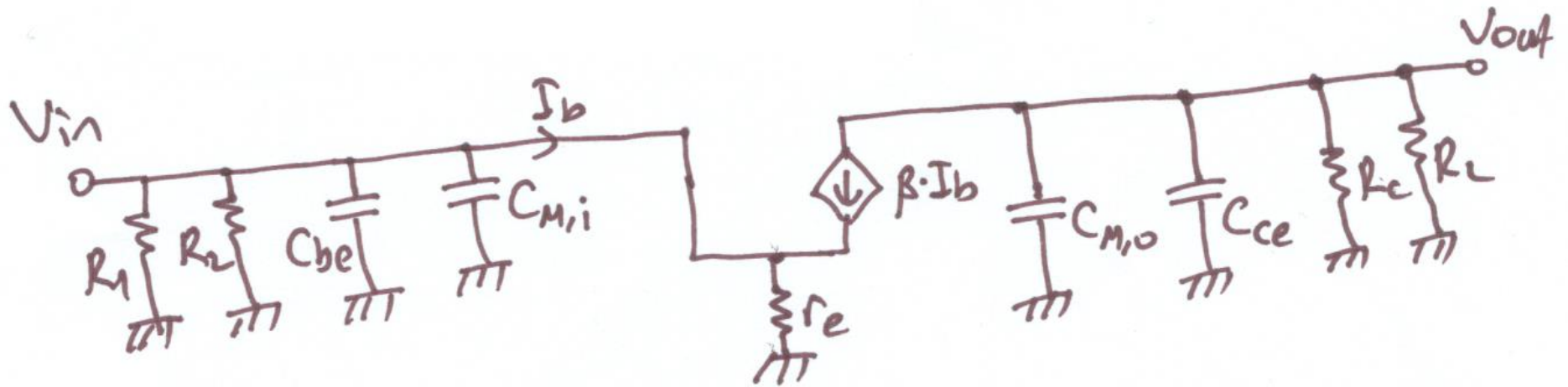


$$f_{c4} = \frac{1}{2 \cdot \pi \cdot (R_1 \parallel R_2 \parallel ((\beta + 1) \cdot r_e)) \cdot (C_{be} + C_{M,i})}$$

$$f_{c5} = \frac{1}{2 \cdot \pi \cdot (R_c \parallel R_L) \cdot (C_{M,o} + C_{ce})}$$



## Common-Emitter Amplifier: High-Frequency Analysis (5)

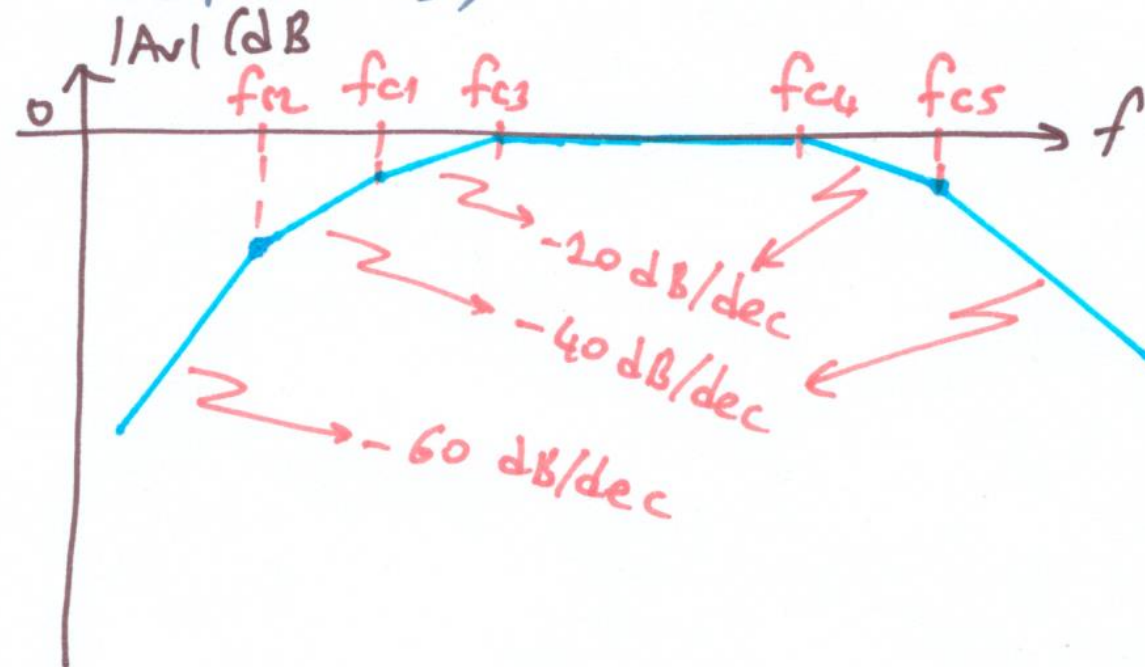


$$f_{c4} = \frac{1}{2 \cdot \pi \cdot (R_1 \parallel R_2 \parallel ((\beta + 1) \cdot r_e)) \cdot (C_{be} + C_{M,i})}$$

$$f_{c5} = \frac{1}{2 \cdot \pi \cdot (R_c \parallel R_L) \cdot (C_{M,o} + C_{ce})}$$

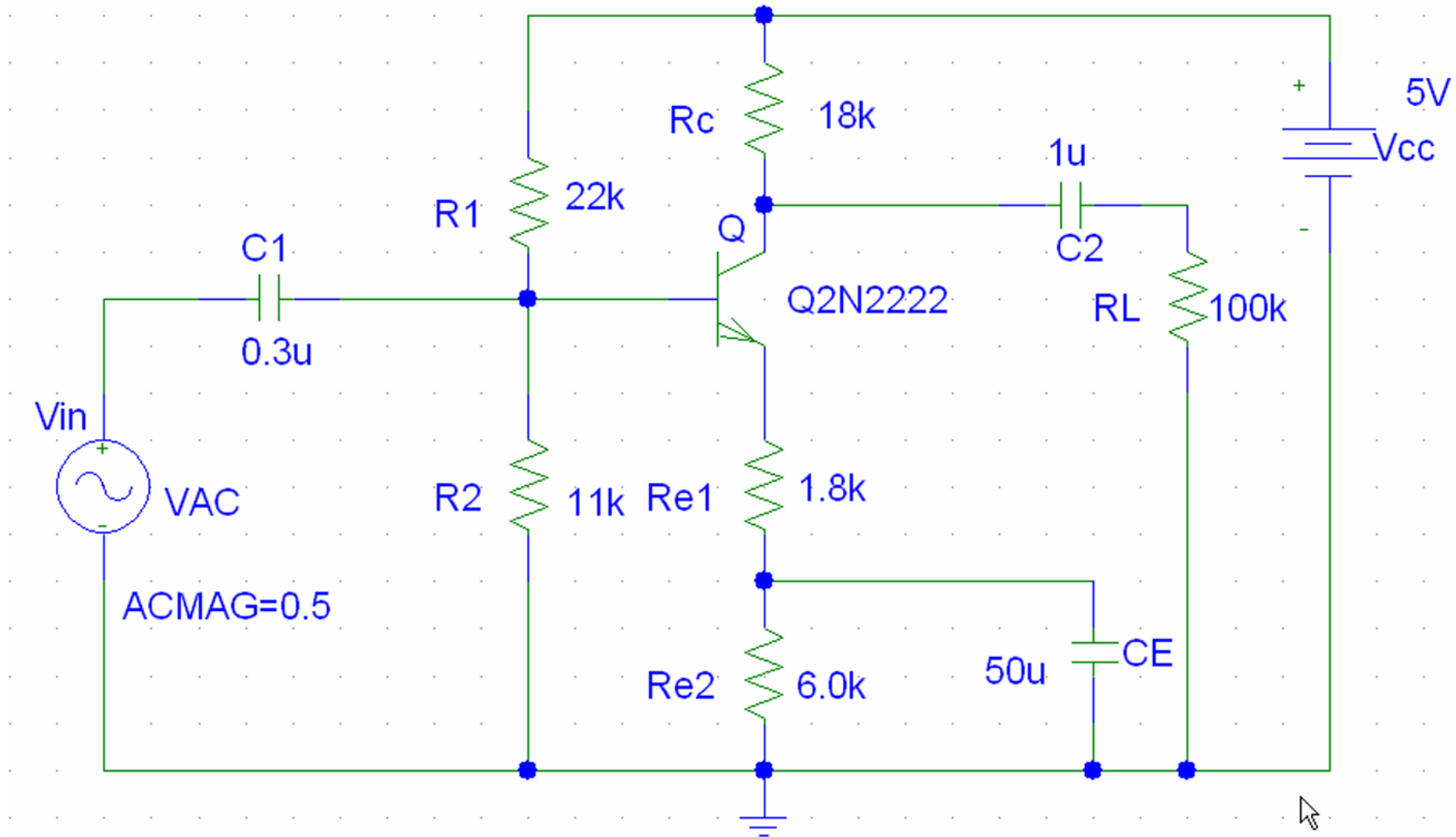
## Common-Emitter Amplifier: High-Frequency Analysis (6)

If  $f_{c4} < f_{c5}$ ,



The complete frequency-response of a common-emitter BJT amplifier.

# Common-Emitter Amplifier: LTspice Example (1)



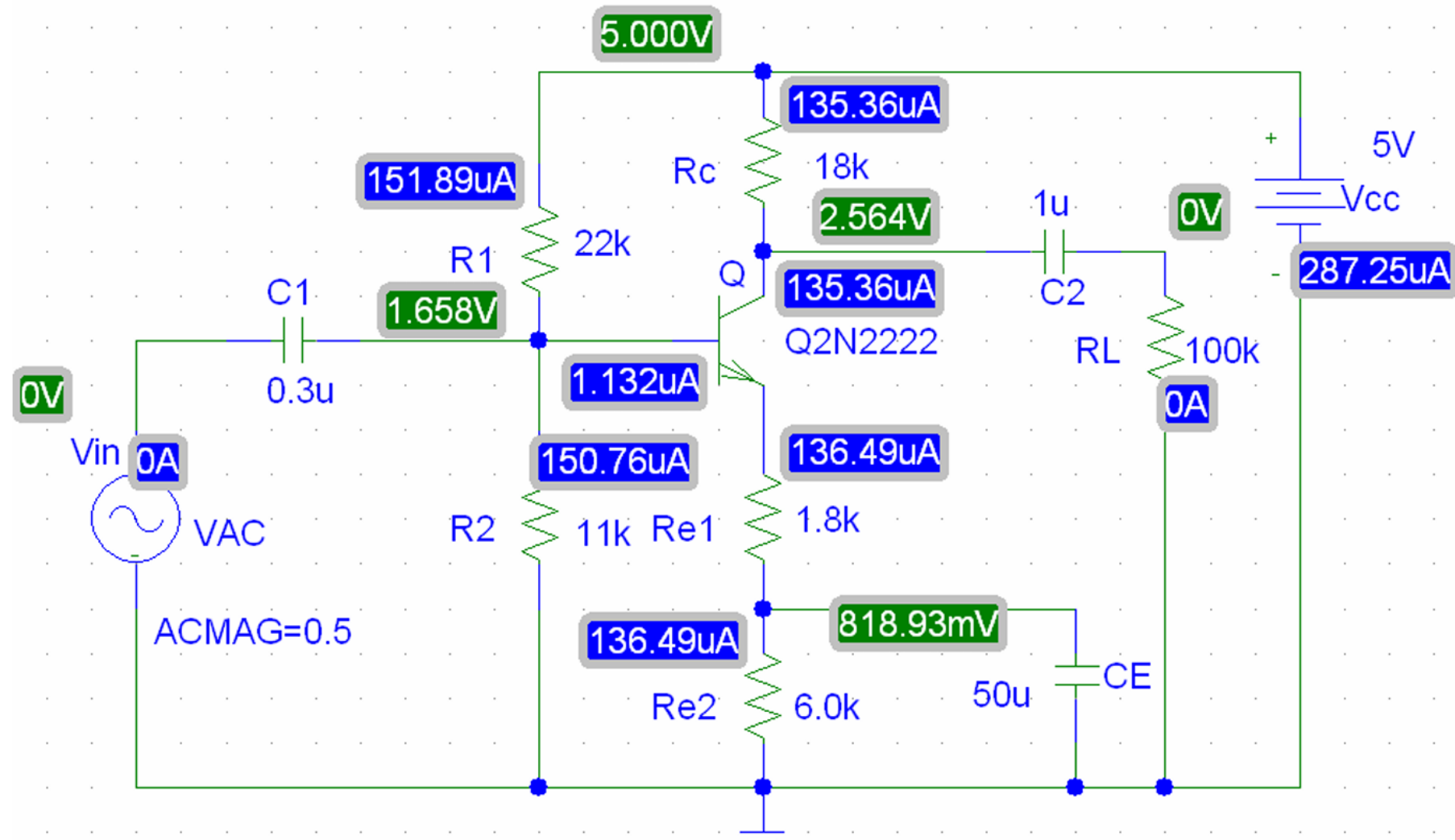
## Common-Emitter Amplifier: LTspice Example (2)

Note that:

- (a) Input signal is  $V_{AC}$  (instead of usual  $V_{SIN}$ ) in the circuit since we need frequency response of the output voltage. AC Sweep is done only with  $V_{AC}$  with only amplitude specified. Different frequency will be applied by the simulator.
- (b) To ease the limitation of power supply by IOBoard,  $V_{CC}$  is supplied by 5V source.
- (c) The load resistance in the circuit is chosen  $100k\Omega$ . This value may be significantly different from your assigned work. You, I mean, you need to do some work too. Right?
- (d) Why those values of resistors? Try to answer by DC analysis. Find expected voltage gain from the DC analysis.
- (e) Why those values of capacitors? Perform AC analysis and find expected low and high cutoff frequencies.



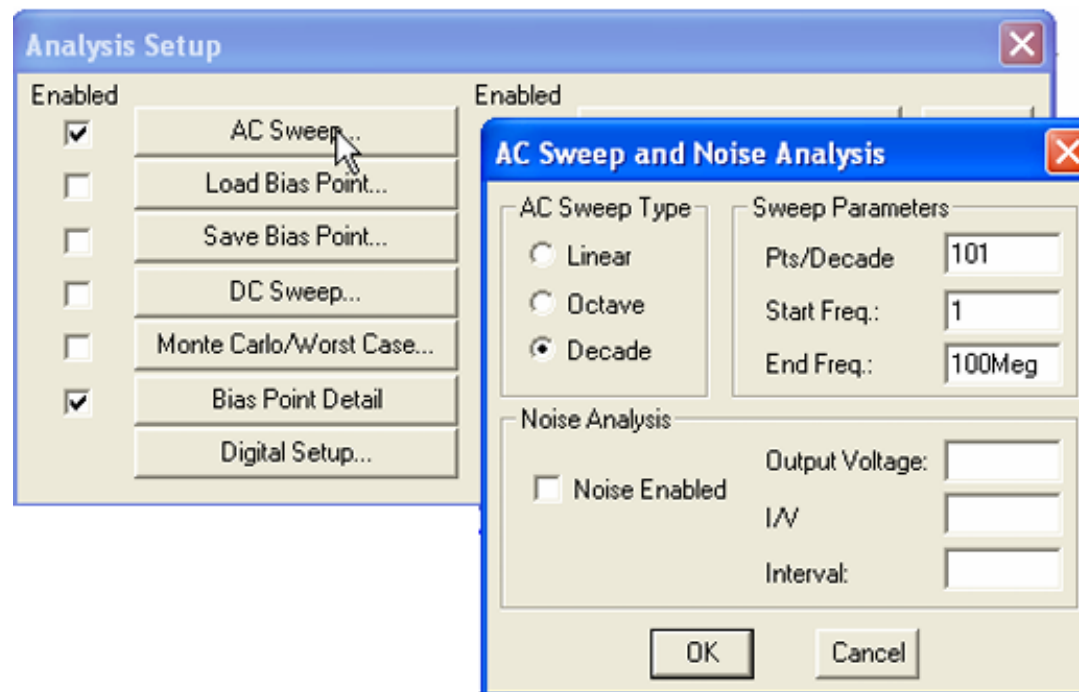
# Common-Emitter Amplifier: LTspice Example (3)



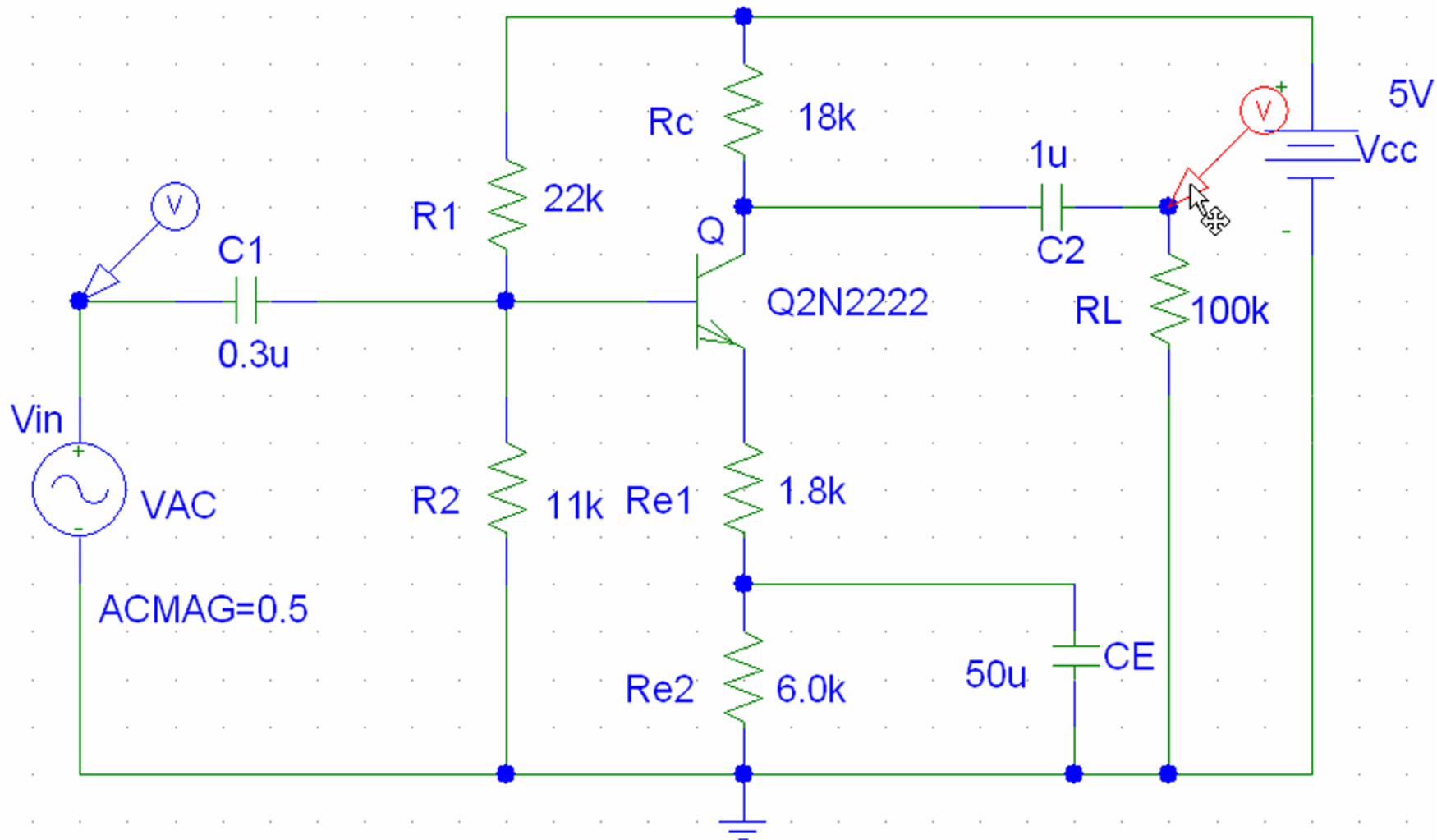
## Common-Emitter Amplifier: LTspice Example (4)

### AC Sweep

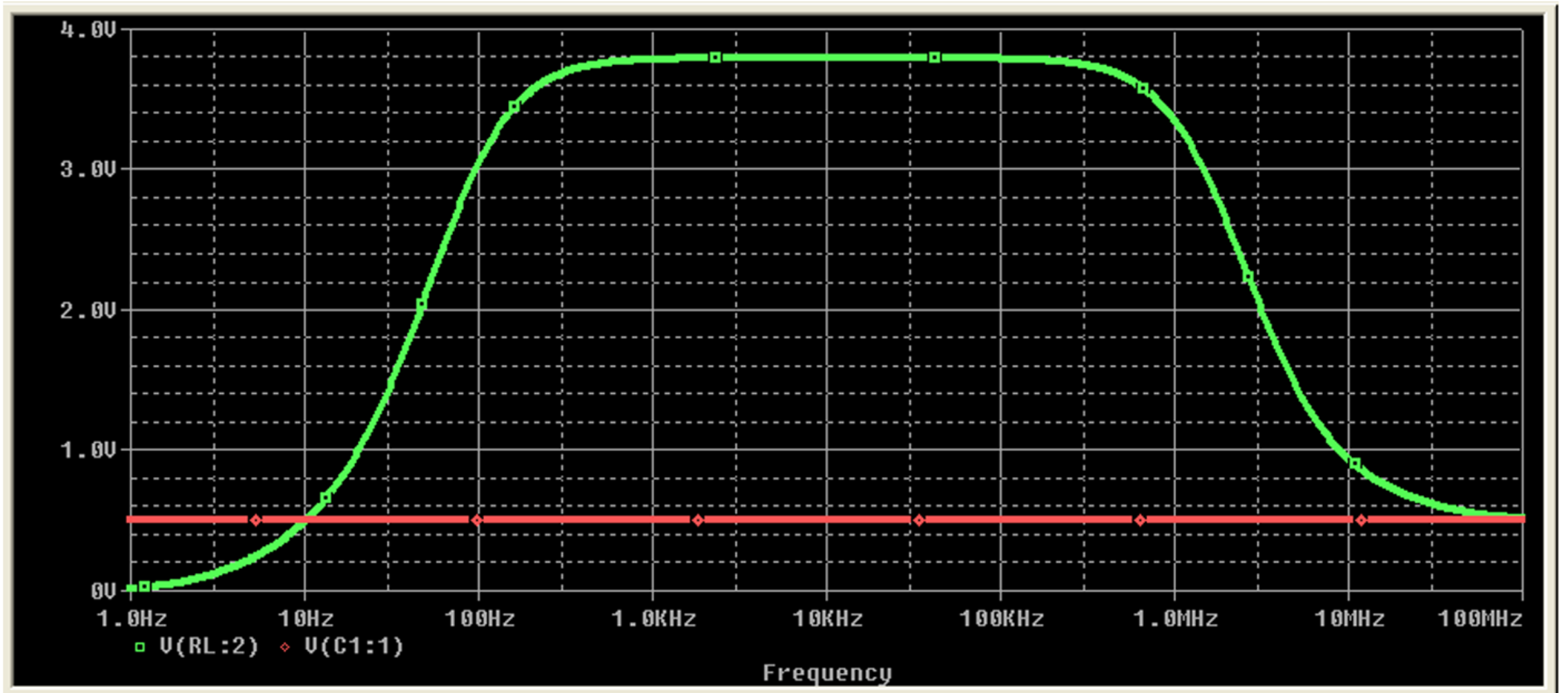
To do AC Sweep, your AC source must be VAC. You decide only ACMAG (AC magnitude), and I picked 0.5 in the circuit above. In the AC Sweep we have to assign our frequency band of interest. Here I set from 0 to 100MHz.



## Common-Emitter Amplifier: LTspice Example (5)



## Common-Emitter Amplifier: LTspice Example (6)



## Common-Emitter Amplifier: LTspice Example (7)

The green tracing is the voltage at the load and red, the input voltage, at each of the frequency at the range of 0 - 100MHz. What is the low cutoff frequency? What is the high cutoff frequency? What is the mid-band voltage gain? How can you change the cutoff frequencies by changing capacitor values? How do you change the gain by changing resistor values? Do you see the influence of load, so called "load effect"?



# Common-Emitter Amplifier: 2N2222 Catalogue (1)

## Absolute Maximum Ratings ( $T_a = 25\text{ }^\circ\text{C}$ )

Parameter		Symbol	Value	Unit
Collector Base Voltage	2N2222	$V_{CBO}$	60	V
	2N2222A		75	
Collector Emitter Voltage	2N2222	$V_{CEO}$	30	V
	2N2222A		40	
Emitter Base Voltage	2N2222	$V_{EBO}$	5	V
	2N2222A		6	
Collector Current		$I_C$	600	mA
Power Dissipation		$P_{tot}$	625	mW
Junction Temperature		$T_j$	150	$^\circ\text{C}$
Storage Temperature Range		$T_{stg}$	- 55 to + 150	$^\circ\text{C}$

## Common-Emitter Amplifier: 2N2222 Catalogue (2)

### Characteristics at $T_a = 25\text{ }^\circ\text{C}$

Parameter		Symbol	Min.	Max.	Unit
DC Current Gain at $V_{CE} = 10\text{ V}$ , $I_C = 0.1\text{ mA}$ at $V_{CE} = 10\text{ V}$ , $I_C = 1\text{ mA}$ at $V_{CE} = 10\text{ V}$ , $I_C = 10\text{ mA}$ at $V_{CE} = 10\text{ V}$ , $I_C = 150\text{ mA}$ at $V_{CE} = 10\text{ V}$ , $I_C = 500\text{ mA}$		$h_{FE}$	35	-	-
		$h_{FE}$	50	-	-
		$h_{FE}$	75	-	-
		$h_{FE}$	100	300	-
	2N2222	$h_{FE}$	30	-	-
2N2222A	$h_{FE}$	40	-	-	
Collector Base Cutoff Current at $V_{CB} = 50\text{ V}$ at $V_{CB} = 60\text{ V}$	2N2222	$I_{CBO}$	-	10	nA
	2N2222A		-	10	
Collector Base Breakdown Voltage at $I_C = 10\text{ }\mu\text{A}$	2N2222	$V_{(BR)CBO}$	60	-	V
	2N2222A		75	-	

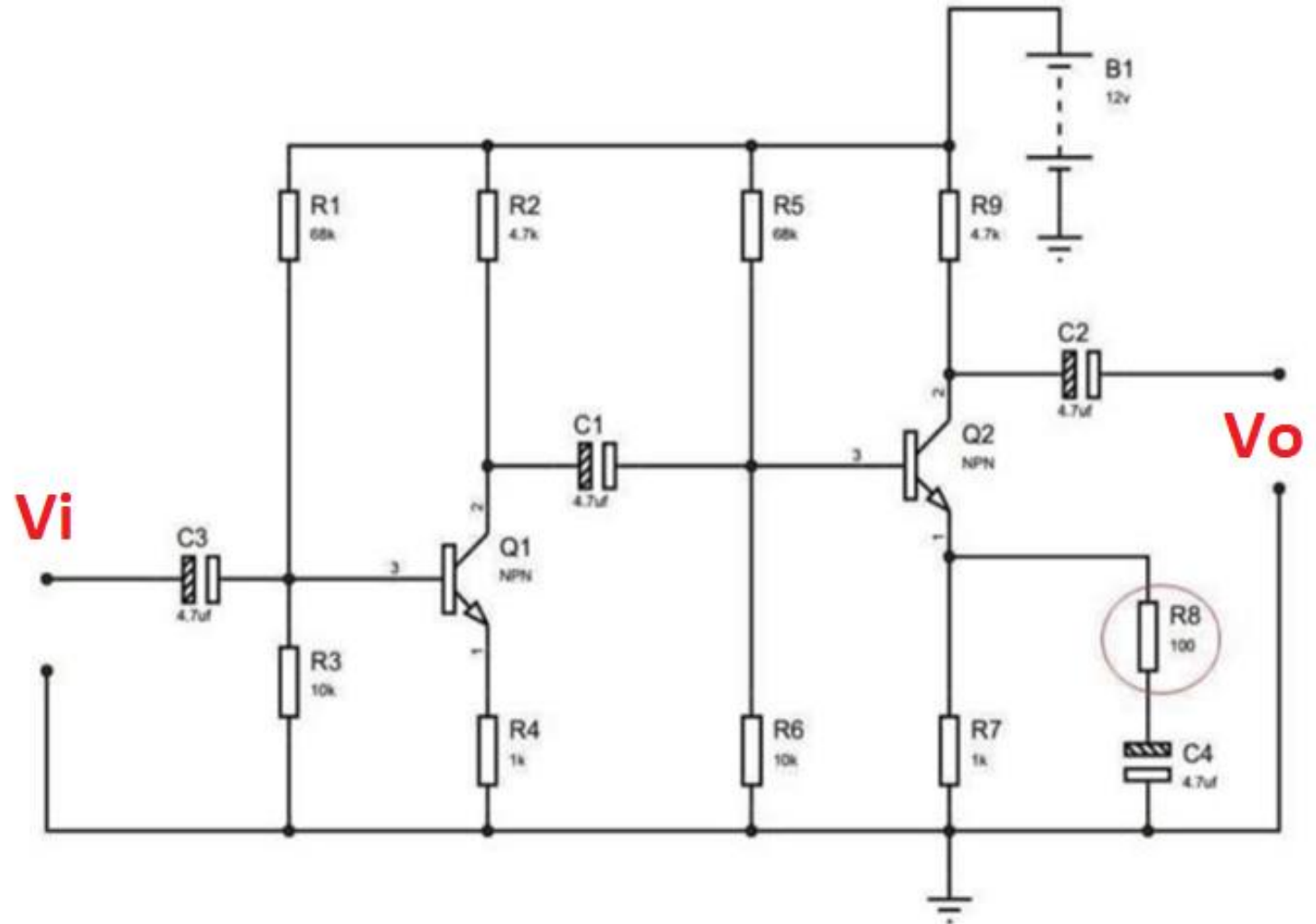
## Common-Emitter Amplifier: 2N2222 Catalogue (3)

### Characteristics at $T_a = 25\text{ }^\circ\text{C}$

Parameter		Symbol	Min.	Max.	Unit
Collector Emitter Saturation Voltage at $I_C = 150\text{ mA}$ , $I_B = 15\text{ mA}$	2N2222	$V_{CE(sat)}$	-	0.4	V
	2N2222A		-	0.3	
at $I_C = 500\text{ mA}$ , $I_B = 50\text{ mA}$	2N2222		-	1.6	
	2N2222A		-	1	
Base Emitter Saturation Voltage at $I_C = 150\text{ mA}$ , $I_B = 15\text{ mA}$	2N2222	$V_{BE(sat)}$	-	1.3	V
	2N2222A		0.6	1.2	
at $I_C = 500\text{ mA}$ , $I_B = 50\text{ mA}$	2N2222		-	2.6	
	2N2222A		-	2	
Gain Bandwidth Product at $I_C = 20\text{ mA}$ , $V_{CE} = 20\text{ V}$ , $f = 100\text{ MHz}$		$f_T$	250	-	MHz

## Left to Students

- If we add an extra resistor ( $R_s$ ) in series of the  $V_{in}$ , what will be change in equations?
- If we remove the  $C_E$  capacitor, what will be change in equations?
- If we connect two amplifiers in cascade connection (see the circuit on the right), what will be change in equations? Is there any change in corner frequencies?





Thanks for  
listening 😊

YALÇIN İŞLER

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